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02/27/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/083.416	02/27/2002	Tsuvoshi Fujiwara	XA-9630	7388

TITLE OF INVENTION: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE COMPRISING A MEMORY CELL AND A **CAPACITOR**

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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NGUYEN	, THANH T	2813	438-253000		
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Renesas Technology Corp.

Tokyo, Japan

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